Complementary power transistors

Features

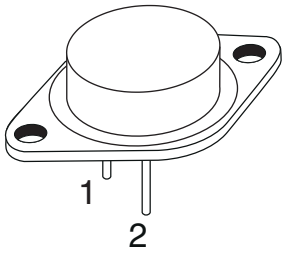
- Low collector-emitter saturation voltage
- Complementary NPN - PNP transistors

Applications

- General purpose
- Audio Amplifier

Description

The devices are manufactured in epitaxial-base planar technology and are suitable for audio, power linear and switching applications.



TO-3

Figure 1. Internal schematic diagram

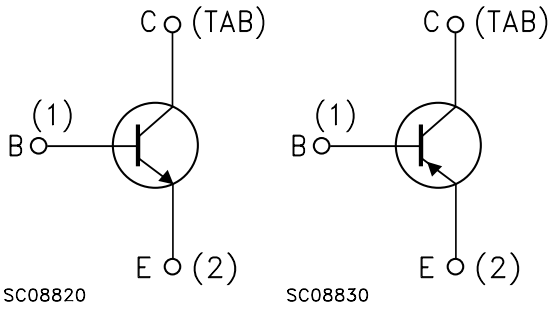


Table 1. Device summary

Order code	Marking	Package	Packaging
2N3055	2N3055	TO-3	tray
MJ2955	MJ2955		

1 Absolute maximun rating

Table 2. Absolute maximum rating

Symbol	Parameter	Value		Unit
		NPN	2N3055	
		PNP	MJ2955	
V_{CBO}	Collector-emitter voltage (I_E = 0)		100	V
V_{CER}	Collector-emitter voltage (R_{BE} = 100 Ω)		70	V
V_{CEO}	Collector-emitter voltage (I_B = 0)		60	V
V_{EBO}	Collector-base voltage (I_C = 0)		7	V
I_C	Collector current		15	A
I_B	Base current		7	A
P_{TOT}	Total dissipation at $T_c \leq 25°C$		115	W
T_{stg}	Storage temperature		-65 to 200	°C
T_J	Max. operating junction temperature		200	°C

Note: For PNP type voltage and current values are negative

2 Electrical characteristics

(T_{case} = 25°C; unless otherwise specified)

Table 3. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_{CEX}	Collector cut-off current (V_{BE} = -1.5 V)	V_{CE} = 100 V V_{CE} = 100 V T_C = 150 °C			1 5	mA mA
I_{CEO}	Collector cut-off current (I_B = 0)	V_{CE} = 30 V			0.7	mA
I_{EBO}	Emitter cut-off current (I_C = 0)	V_{EB} = 7 V			5	mA
$V_{CEO(sus)}$ (1)	Collector-emitter sustaining voltage (I_B = 0)	I_C = 200 mA	60			V
$V_{CER(sus)}$ (1)	Collector-emitter sustaining voltage (R_{BE} = 100 Ω)	I_C = 200 mA	70			V
$V_{CE(sat)}$ (1)	Collector-emitter saturation voltage	I_C = 4 A I_B = 400 mA I_C = 10 A I_B = 3.3 A			1 3	V V
V_{BE} (1)	Base-emitter voltage	I_C = 4 A V_{CE} = 4 V			1.8	V
h_{FE} (1)	DC current gain	I_C = 4 A V_{CE} = 4 V I_C = 10 A V_{CE} = 4 V	20 5		70	

1. *Pulsed : Pulse duration = 300 µs, duty cycle ≤1.5%*

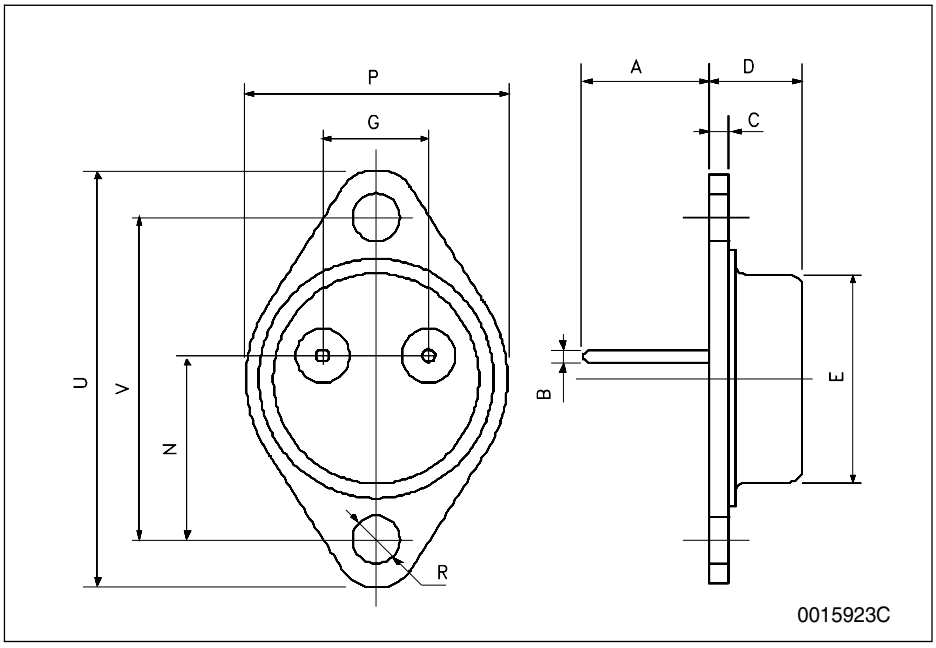
Note: For PNP type voltage and current values are negative

3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

TO-3 mechanical data

DIM.	mm.		
	min.	typ	max.
A	11.00		13.10
B	0.97		1.15
C	1.50		1.65
D	8.32		8.92
E	19.00		20.00
G	10.70		11.10
N	16.50		17.20
P	25.00		26.00
R	4.00		4.09
U	38.50		39.30
V	30.00		30.30



4 Revision history

Table 4. Document revision history

Date	Revision	Changes
11-Oct-1999	6	
29-Jan-2007	7	Content reworked to improve readability, no technical changes

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