

February 1995

LP339 Ultra-Low Power Quad Comparator

General Description

The LP339 consists of four independent voltage comparators designed specifically to operate from a single power supply and draw typically 60 μA of power supply drain current over a wide range of power supply voltages. Operation from split supplies is also possible and the ultra-low power supply drain current is independent of the power supply voltage. These comparators also feature a common-mode range which includes ground, even when operated from a single supply.

Applications include limit comparators, simple analog-to-digital converters, pulse, square and time delay generators; VCO's; multivibrators; high voltage logic gates. The LP339 was specifically designed to interface with the CMOS logic family. The ultra-low supply current makes the LP339 valuable in battery powered applications.

Advantages

Ultra-low power supply drain suitable for battery applications

- Single supply operation
- Sensing at ground
- Compatible with CMOS logic family
- Pin-out identical to LM339

Features

- Ultra-low power supply current drain (60 μ A)—independent of the supply voltage (75 μ W/comparator at \pm 5 V_{DC})
- Low input biasing current

3 nA

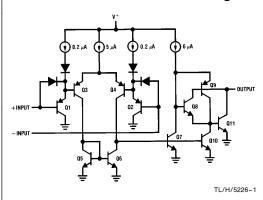
■ Low input offset current

±0.5 nA

■ Low input offset voltage

- \pm 2 mV
- Input common-mode voltage includes ground
- Output voltage compatible with MOS and CMOS logic
- \blacksquare High output sink current capability (30 mA at $V_{O}\!=\!2\,V_{DC}$)
- Supply Input protected against reverse voltages

Schematic and Connection Diagrams



OUTPUT 3 OUTPUT 4 GND INPUT 4+ INPUT 4- INPUT 3+ INPUT 3
12 11 10 9 8

0 17 12 11 10 9 8

OUTPUT 2 OUTPUT 1 V+ INPUT 1- INPUT 1+ INPUT 2- INPUT 2+

TOP VIEW

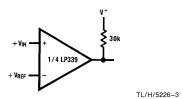
TL/H/5226-2

Order Number LP339M for S.O. Package See NS Package Number M14A

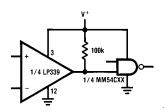
Order Number LP339N for Dual-In-Line Package See NS Package Number N14A

Typical Applications (V $^+$ = 5.0 V_{DC})

Basic Comparator



Driving CMOS



TL/H/5226-4